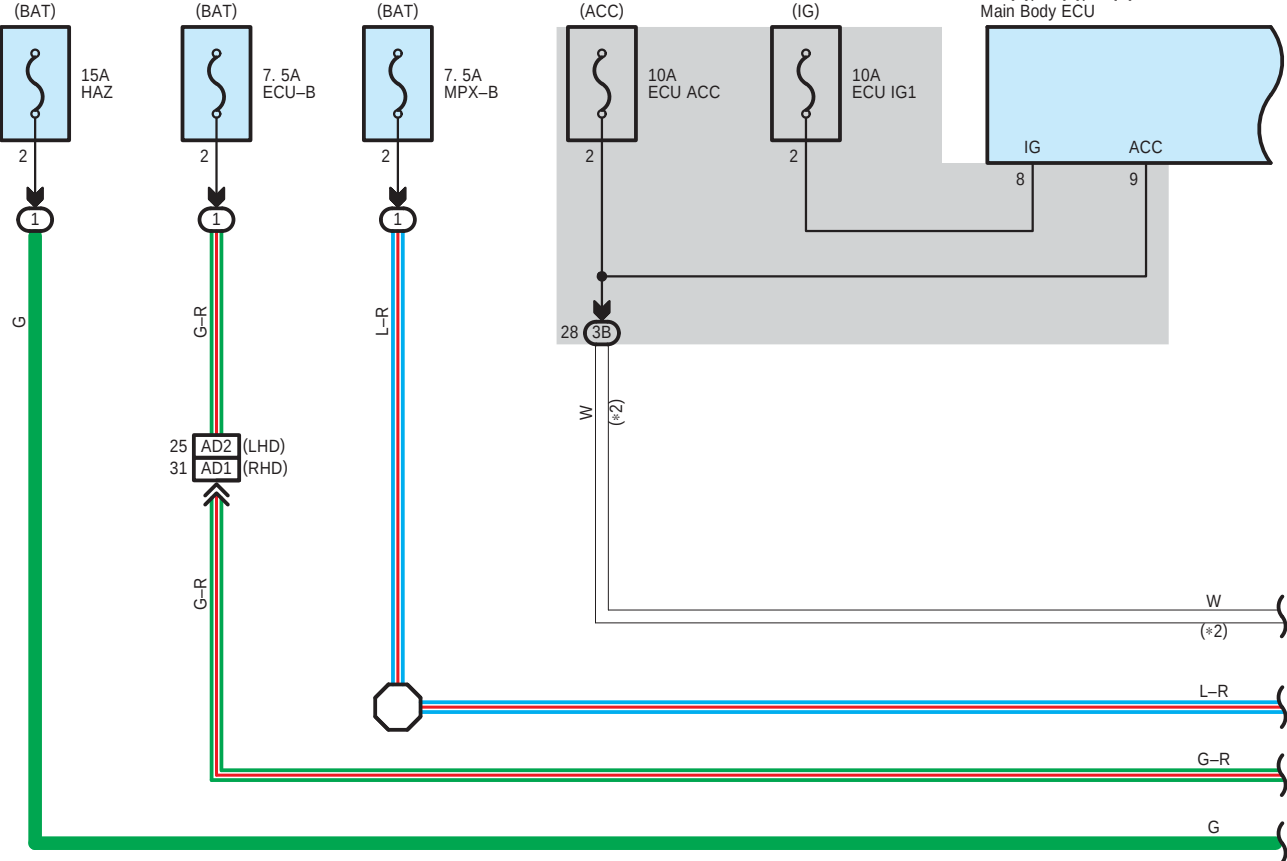
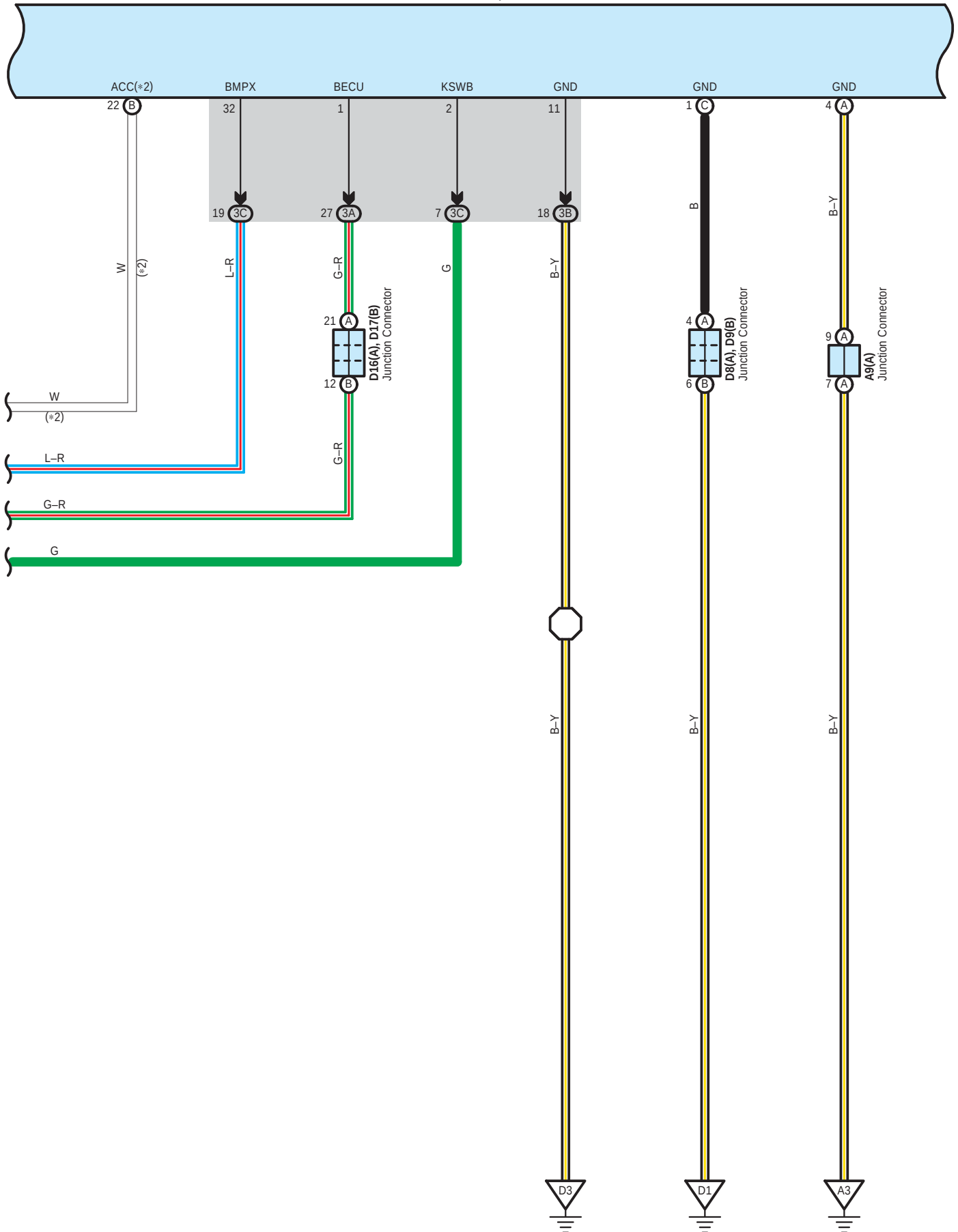




* 2 : w/ Entry & Start System

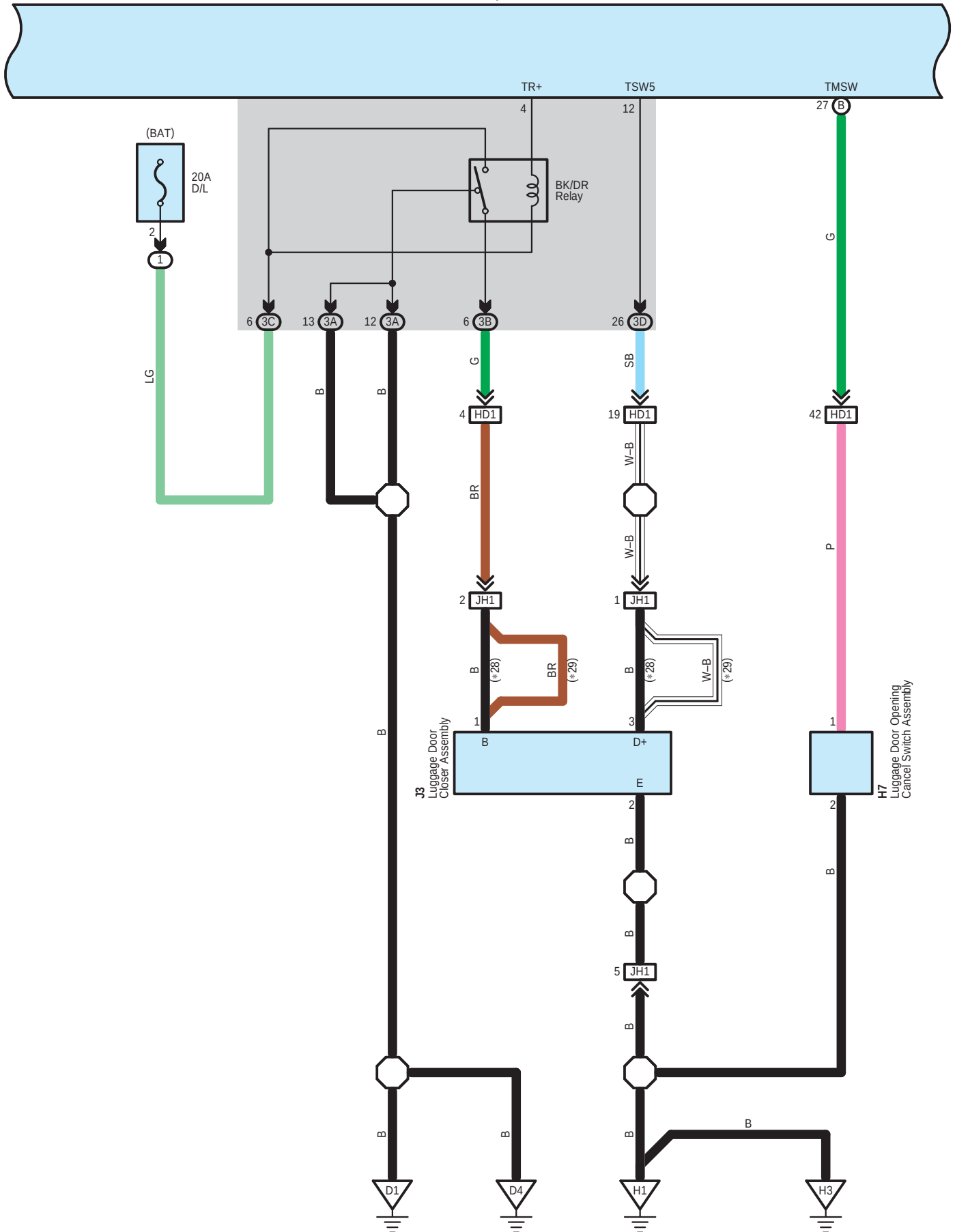
Luggage Compartment Door Opener

A73(A), D5(B), D6(C)
Main Body ECU

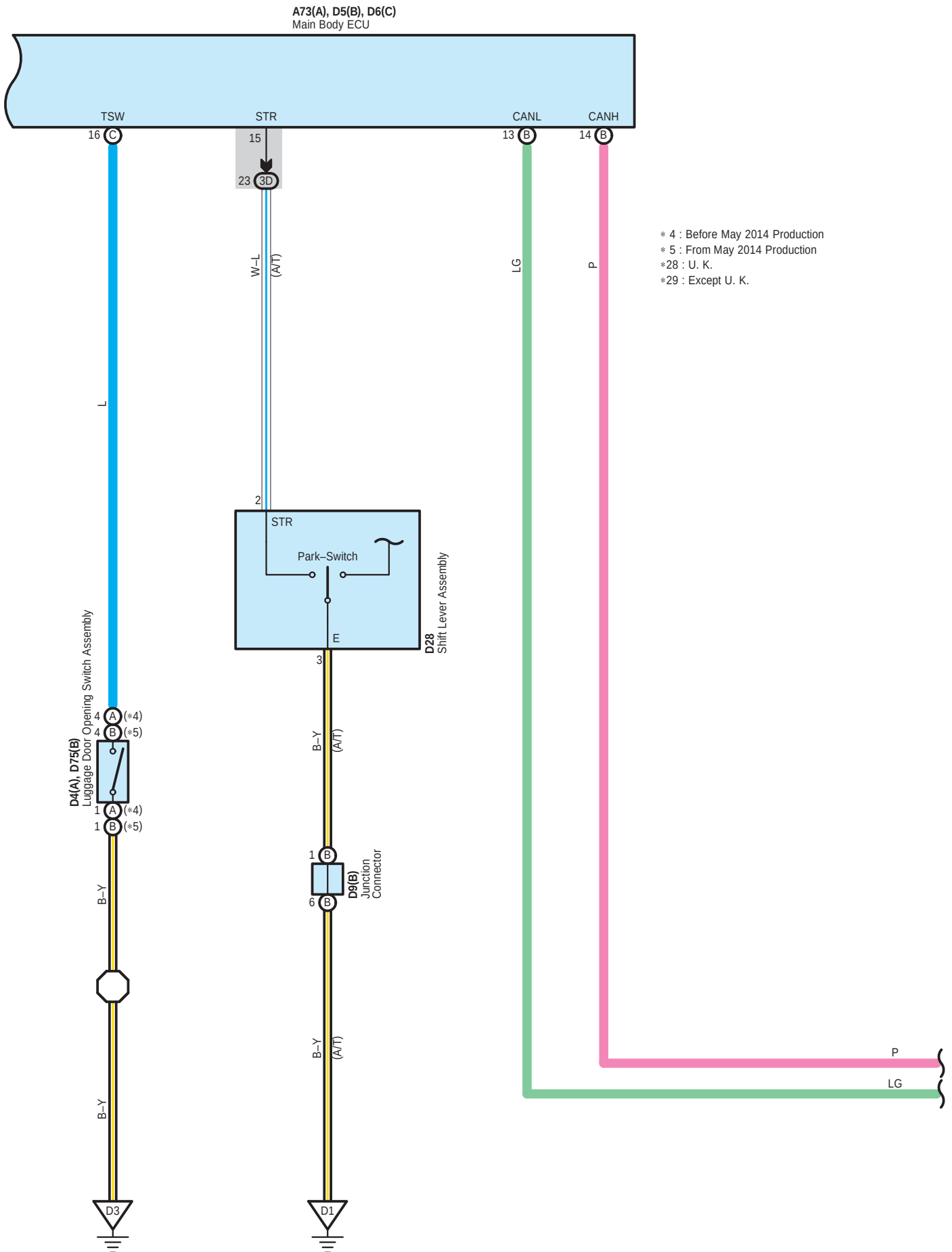


Luggage Compartment Door Opener

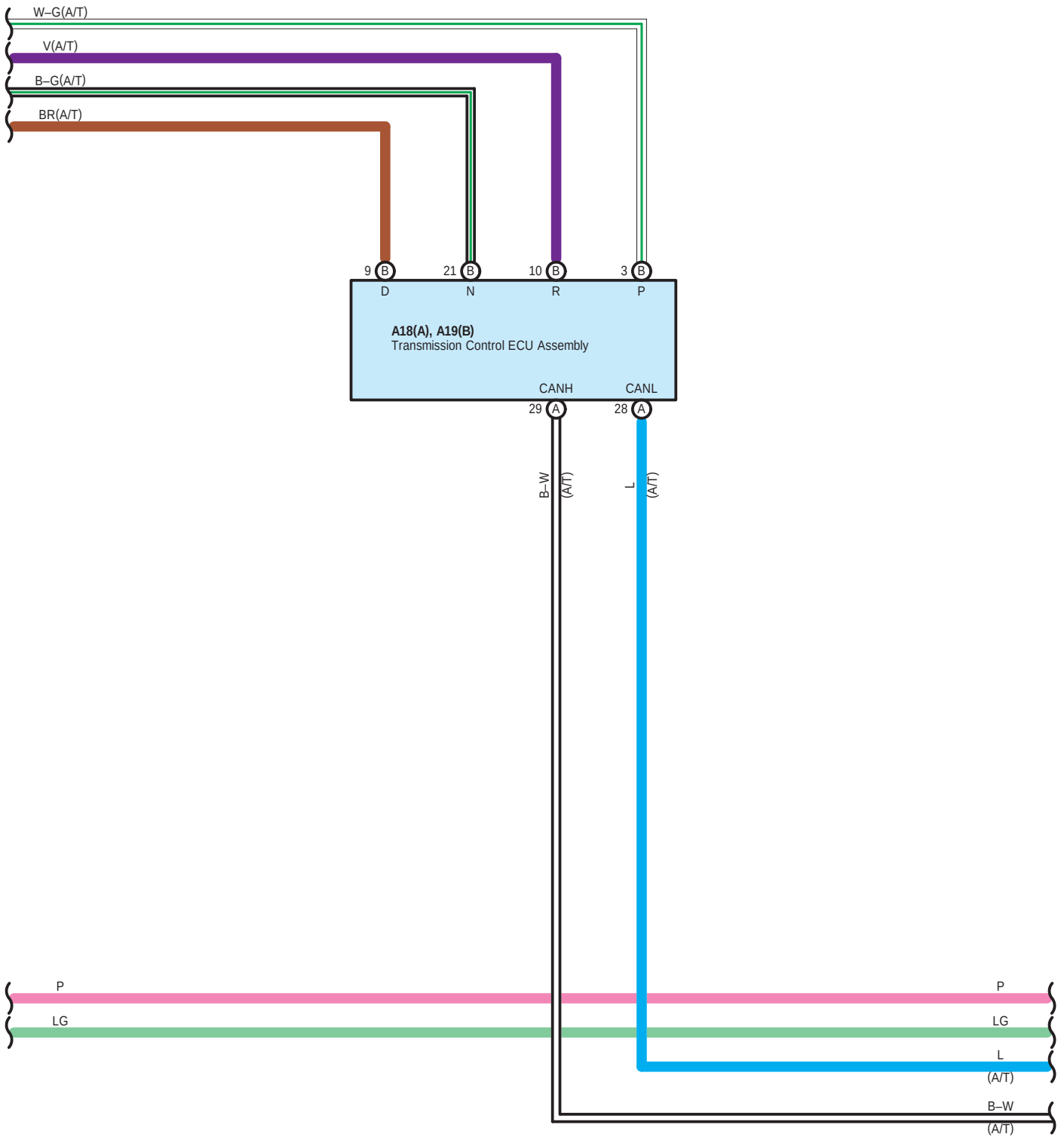
A73(A), D5(B), D6(C)
Main Body ECU



Luggage Compartment Door Opener



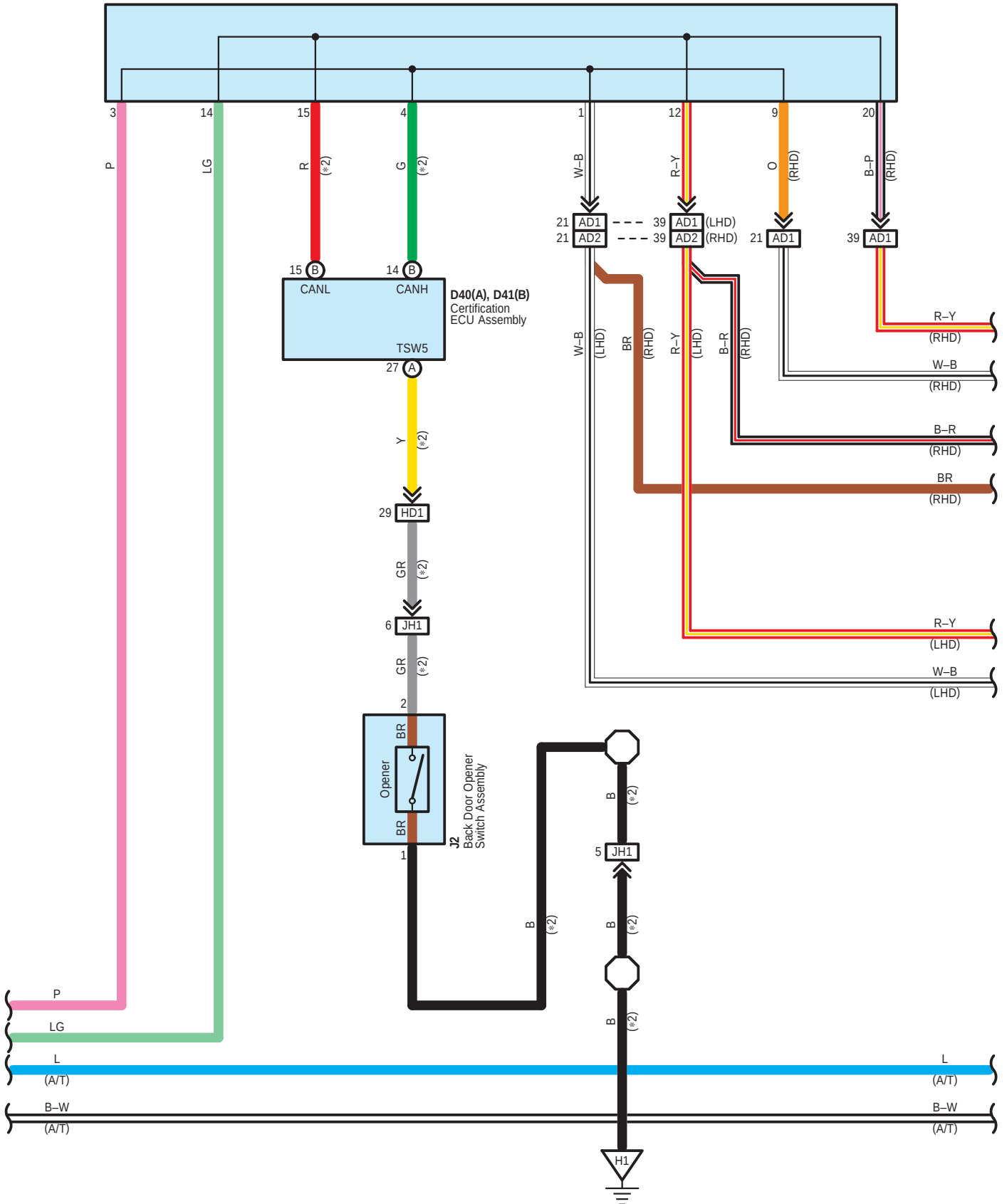




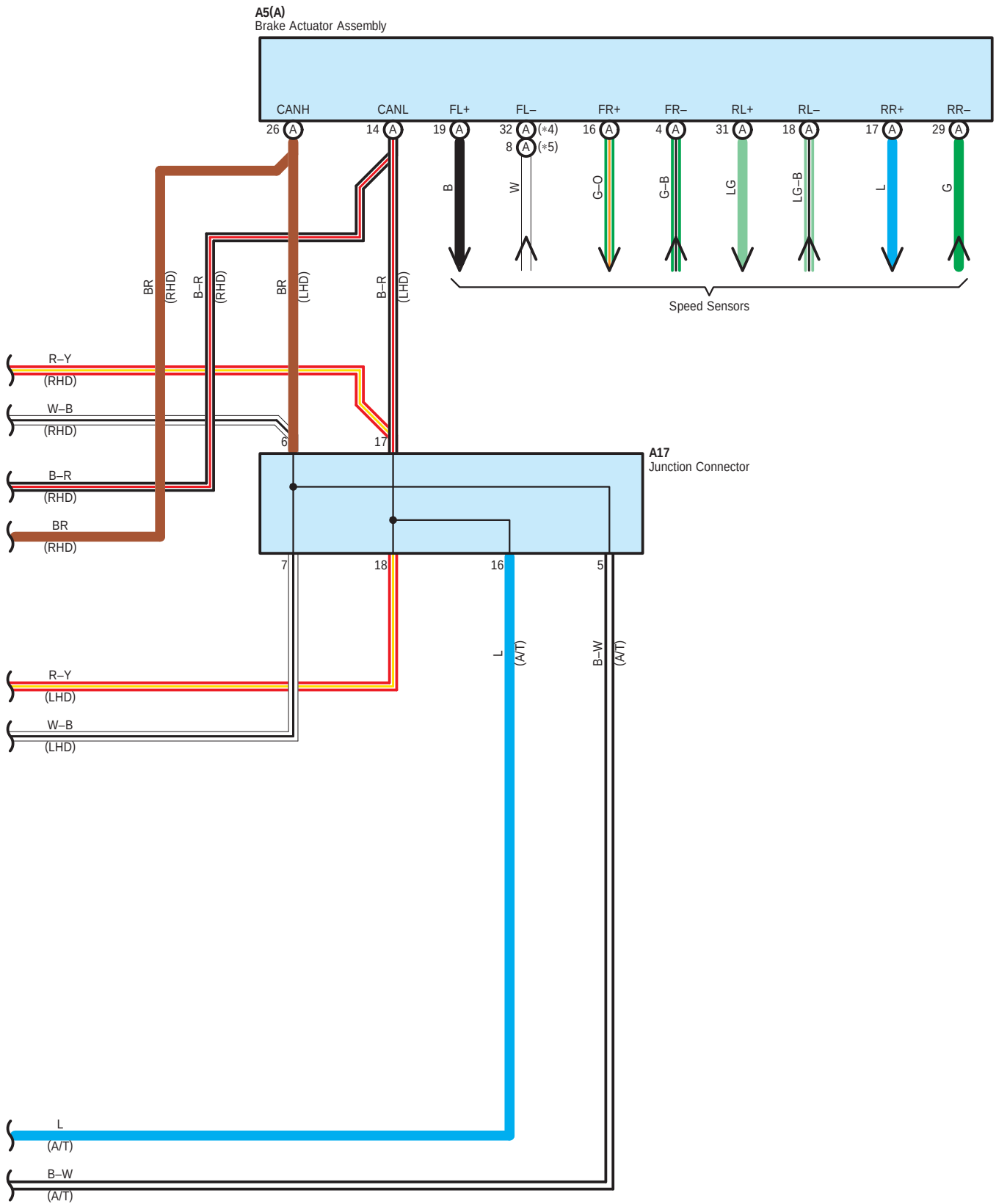
Luggage Compartment Door Opener

- * 2 : w/ Entry & Start System
- * 4 : Before May 2014 Production
- * 5 : From May 2014 Production

D43
Junction Connector



Luggage Compartment Door Opener



Luggage Compartment Door Opener

A 5
White

25	26	27	28	29	30	31	32	33	34	35	36	37	38
1	14	15	16	17	18	19	20	21	22	23	24	13	
	2	3	4	5	6	7	8	9	10	11	12		

A 9
Gray

1	2	3	4	5	6
7	8	9	10	11	12

A 17
Gray

1	2	3	4	5	6	7	8	9	10	11
12	13	14	15	16	17	18	19	20	21	22

A 18
White

1	2				3	4	5	6	7			
8	9	10	11	12	13	14	15	16	17	18	19	
20	21	22	23				24	25			26	27
28	29	30	31				32	33			34	35

A 19
White

	1	2	3	4			5	6		
	7	8	9	10	11	12	13	14	15	16
17	18	19	20	21	22	23	24	25	26	27
28	29	30				31	32	33	34	35

A 73
White

1	2					3	4				
5	6	7	8	9	10	11	12	13	14	15	16
17	18	19	20	21	22	23	24	25	26	27	28

D 4
White

1	2	3	4
---	---	---	---

D 5
White

1			2			3									4			5			6														
7			8			9			10			11			12			13			14			15			16			17			18		
19			20			21			22			23			24			25			26			27			28			29			30		

D 6
White

1									2
3	4	5	6	7	8	9	10		
11	12	13	14	15	16	17	18		

D 8
Blue

1	2	3	4	5	6
7	8	9	10	11	12

D 9
Blue

1	2	3	4	5	6
7	8	9	10	11	12

D 16
Black

1	2	3	4	5	6	7	8	9	10	11
12	13	14	15	16	17	18	19	20	21	22

D 17
Black

1	2	3	4	5	6	7	8	9	10	11
12	13	14	15	16	17	18	19	20	21	22

White

1	2	3			4	5	6
7	8	9	10	11	12	13	14

White

1

2

3

4

5

6

7

8

9

10

11

12

13

14

15

16

17

18

19

20

21

22

23

24

25

26

27

28

White

The diagram shows a 30-slot card rack. It has a central rectangular label holder at the top. Below it, the rack is divided into three rows of ten slots each. The slots are numbered 1 through 30, starting from the top left and moving right across each row, then down to the next row.

1	2	3								4	5	6				
7	8	9	10	11	12	13	14	15	16	17	18					
19	20	21	22	23	24	25	26	27	28	29	30					

Gray

Gray

1	2	3	4	5	6	7	8	9	10	11	
12	13	14	15	16	17	18	19	20	21	22	

White

White

1	2	3	4	5
---	---	---	---	---

White

	1	2
--	---	---

White

Diagram illustrating a 2x2 grid structure. The top row contains the number 2. The bottom row contains the numbers 2 and 1.

White

	1	2	3
--	---	---	---

Grav

Grav

A diagram of a 9-pin D-sub connector. The connector is shown from the front, with a D-shaped shield. The pins are numbered 1 through 9. Pins 1, 2, 3, and 4 are in the top row, and pins 5, 6, 7, 8, and 9 are in the bottom row. The connector is labeled '9-Pin' at the top.

White

The diagram shows a 3x14 grid layout. It is composed of three 3x6 sub-grids and one 3x2 sub-grid. The sub-grids are arranged as follows:

- Sub-grid 1 (Left):** A 3x6 grid containing numbers 1 through 23. The first row has numbers 1-6, the second row has 15-23, and the third row has 36-44.
- Sub-grid 2 (Middle):** A 3x2 grid containing numbers 24 through 47. The first row has numbers 7-8, the second row has 24-26, and the third row has 45-47.
- Sub-grid 3 (Right):** A 3x6 grid containing numbers 9 through 56. The first row has numbers 9-14, the second row has 27-35, and the third row has 48-56.

White

The diagram illustrates a 40-bit bus architecture. It features two 20-bit registers, each represented by a 2x10 grid of cells. The left register contains the values 1 through 31, with the last cell (row 2, column 10) being empty. The right register contains the values 5 through 40, with the last cell (row 2, column 10) being empty. The registers are connected to a central 40-bit bus, which is represented by a horizontal line with a central vertical segment. The bus is labeled '40-bit' at the top. The registers are labeled '20-bit' on their left and right sides. The bus is connected to the registers via 20-bit wide buses, indicated by the '20-bit' labels and the 2x10 grid structure.

White

The diagram shows a 32-bit bus system with two 16-bit registers. The left register contains values 1-16 in the top row and 17-31 in the bottom row. The right register contains values 5-10 in the top row and 17-40 in the bottom row. The bus is labeled 'Data' and 'Address'.

